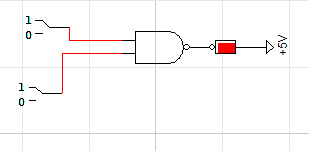
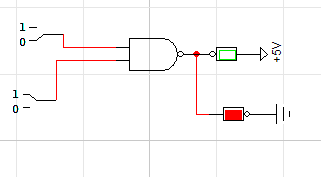
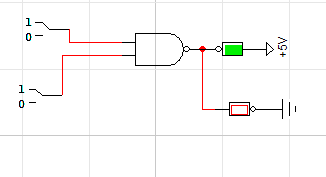
**Question 01:**

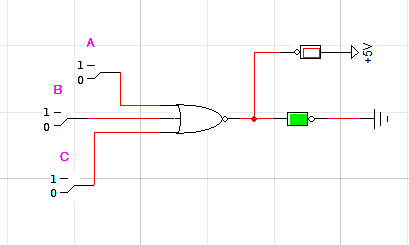


**Question 02:**

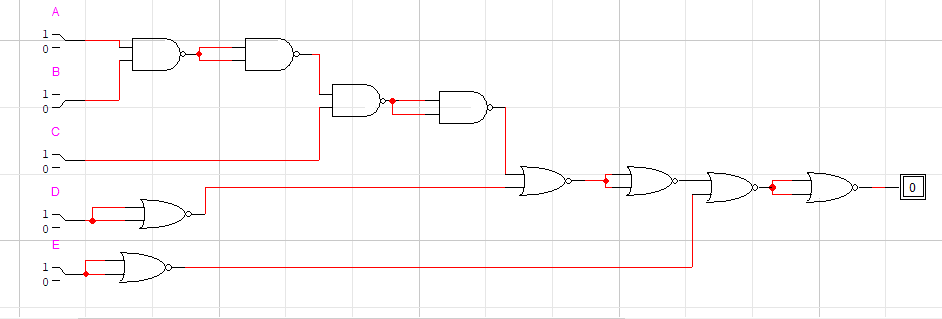




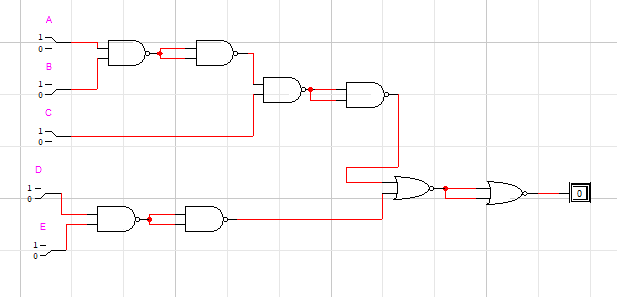
**Question 03**



**Question 04 i)**



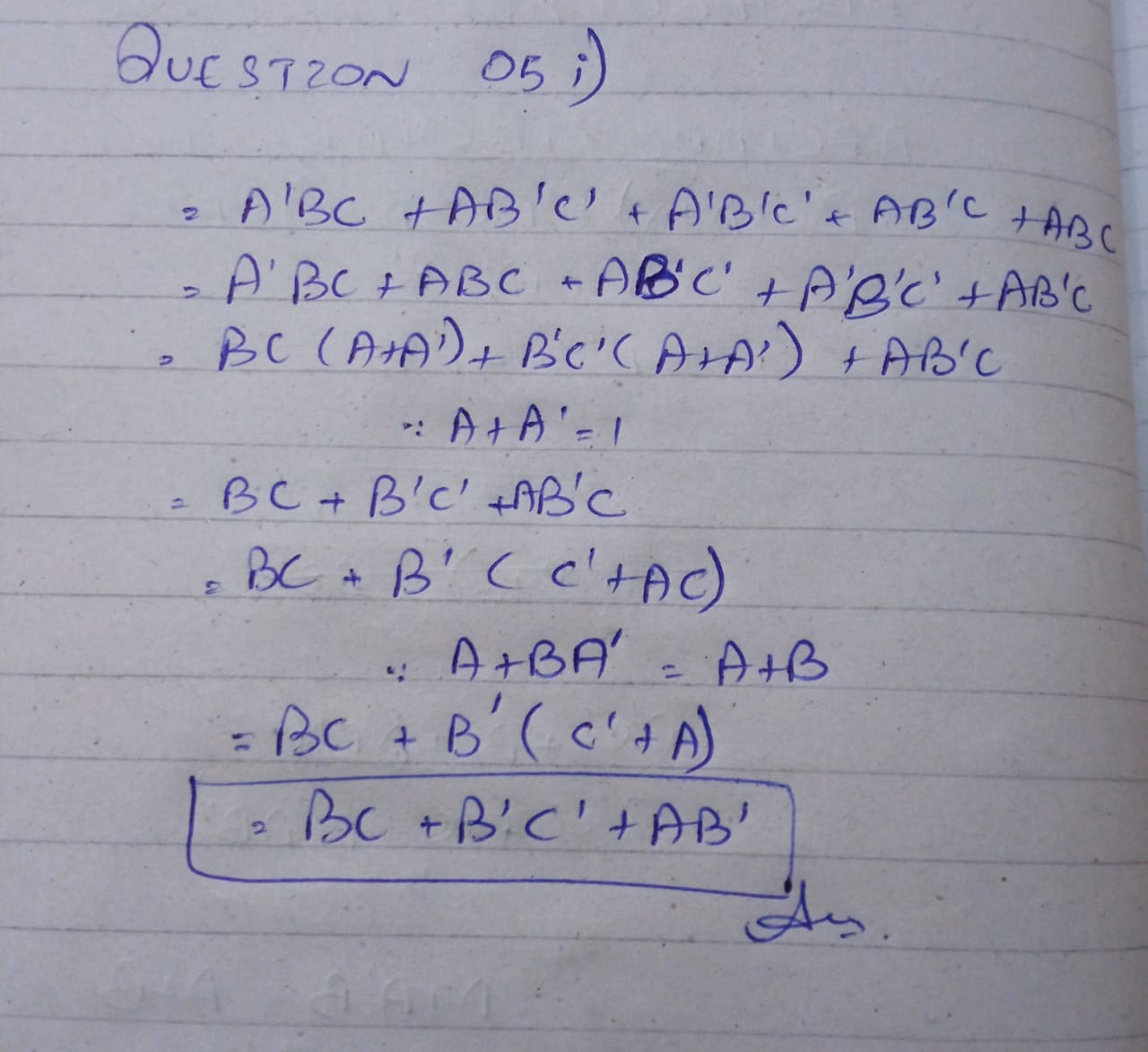
**Question 04 ii)**



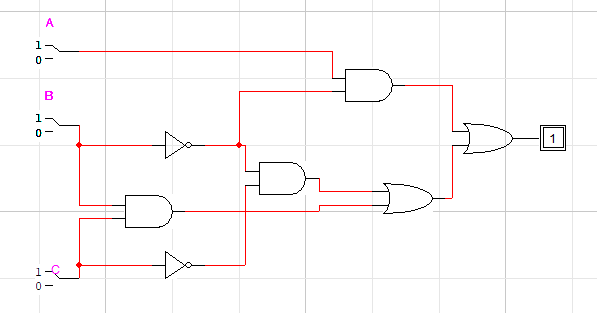
**Question 05**

**Question 05 i)**

Boolean Simplification:

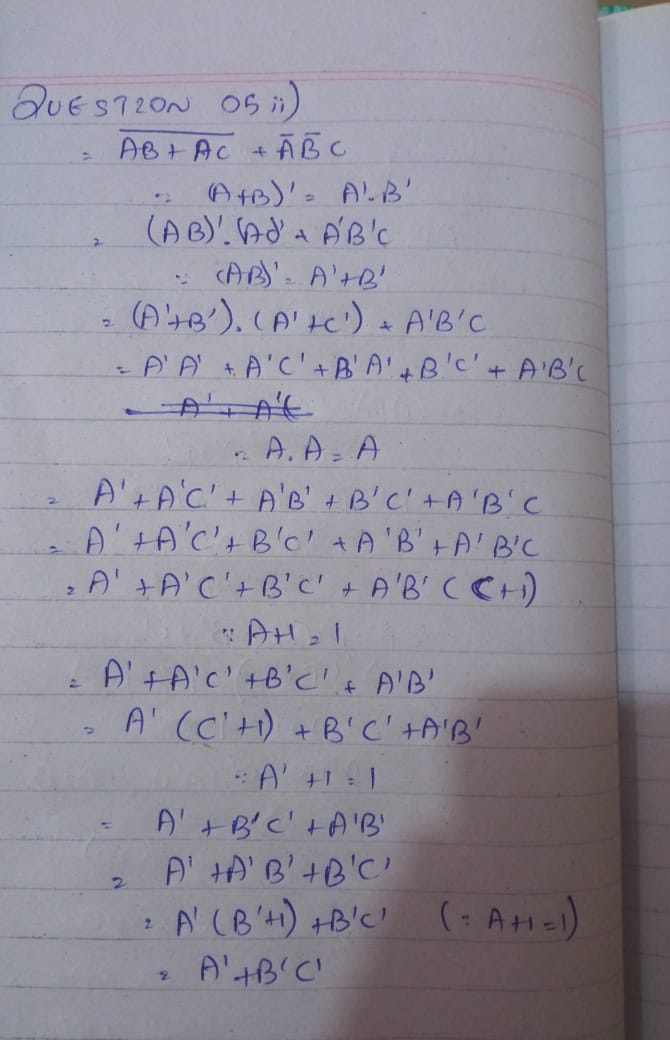
****

**Circuit Diagram:**

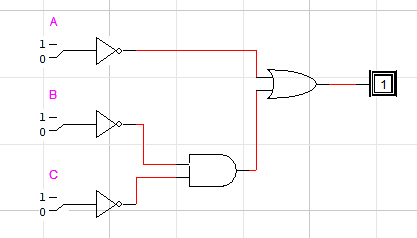
****

**Question ii)**

Boolean Simplification:

**]**

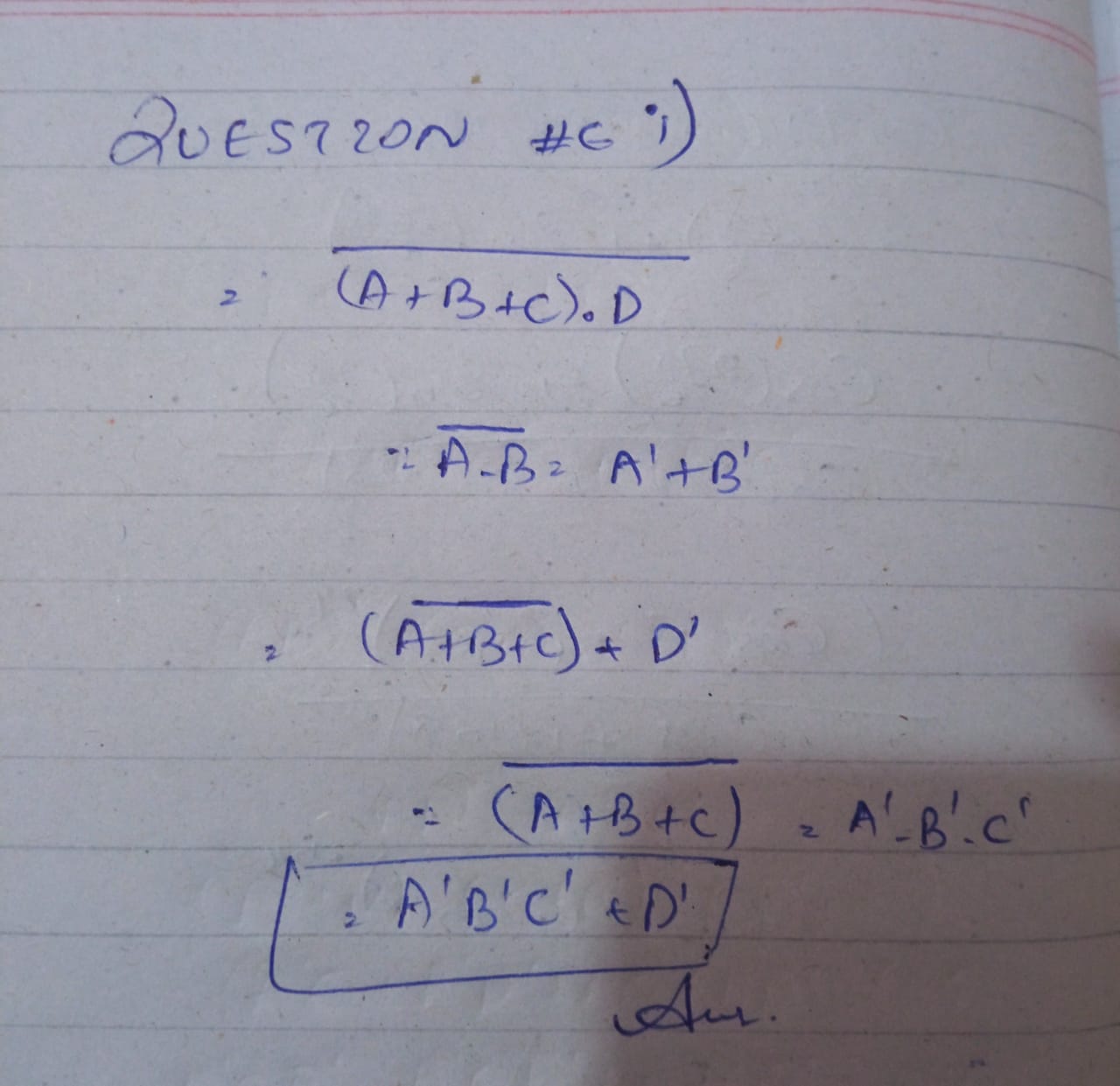
Circuit Diagram:

****

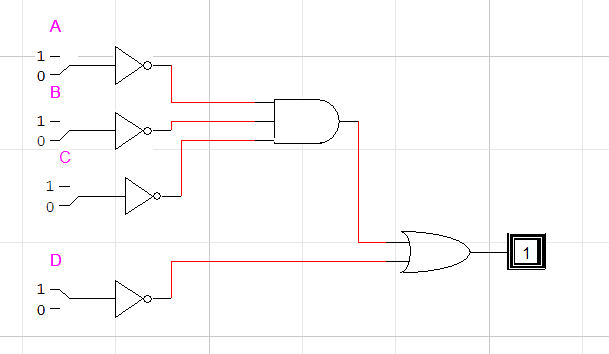
**Question 06**

**Question 06 a)**

Simplification:

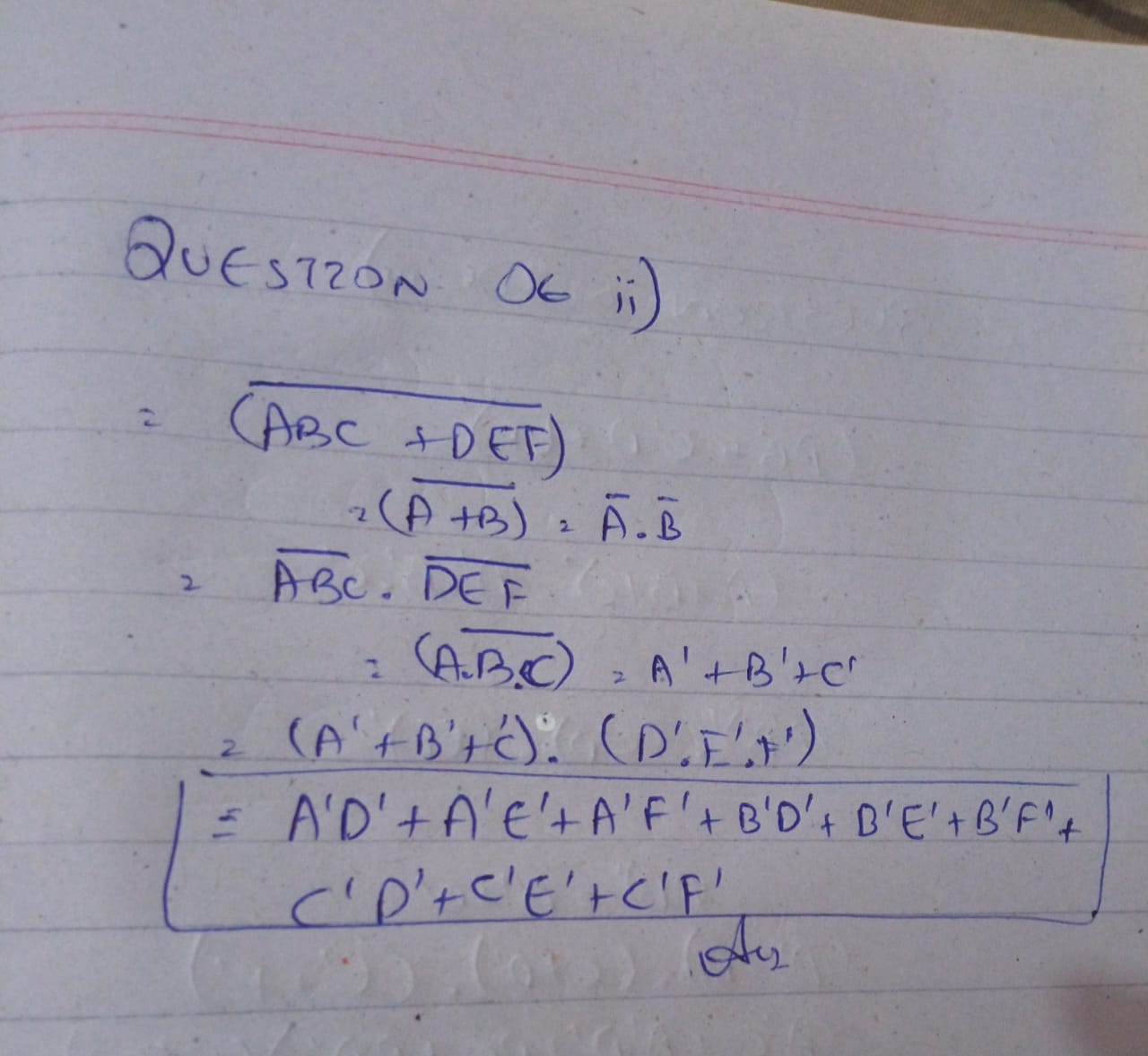
****

Circuit Diagram:

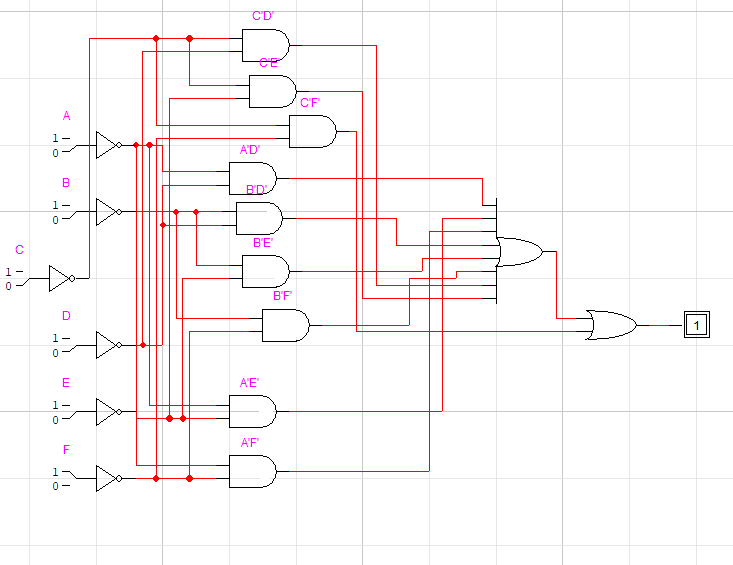


**Question 6b)**

Simplification:

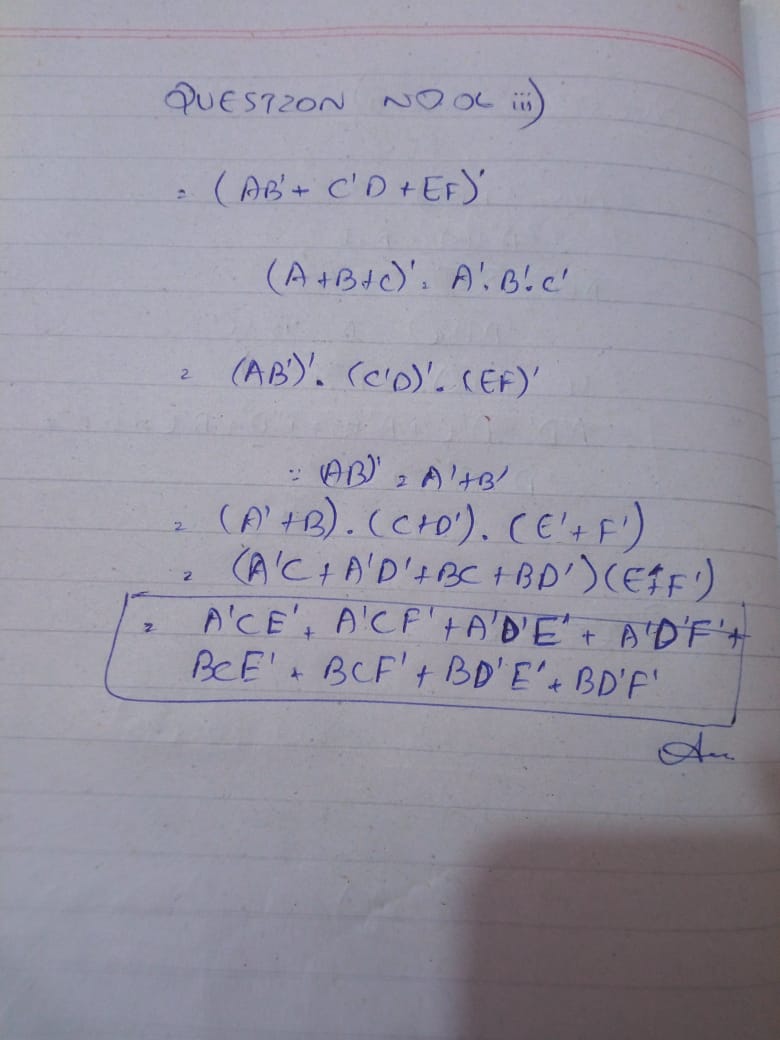
****

Circuit Diagram:

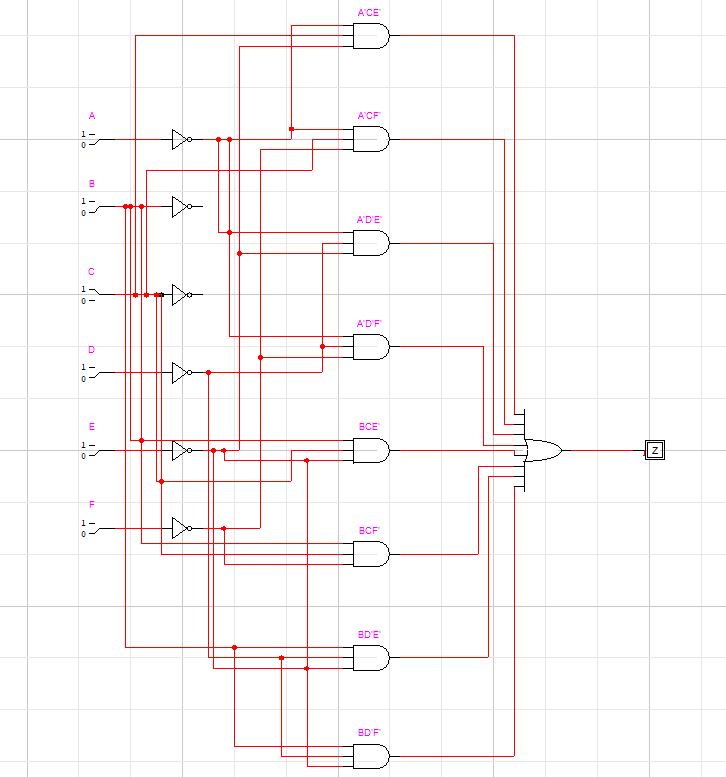


**Question 06c)**

Simplification:

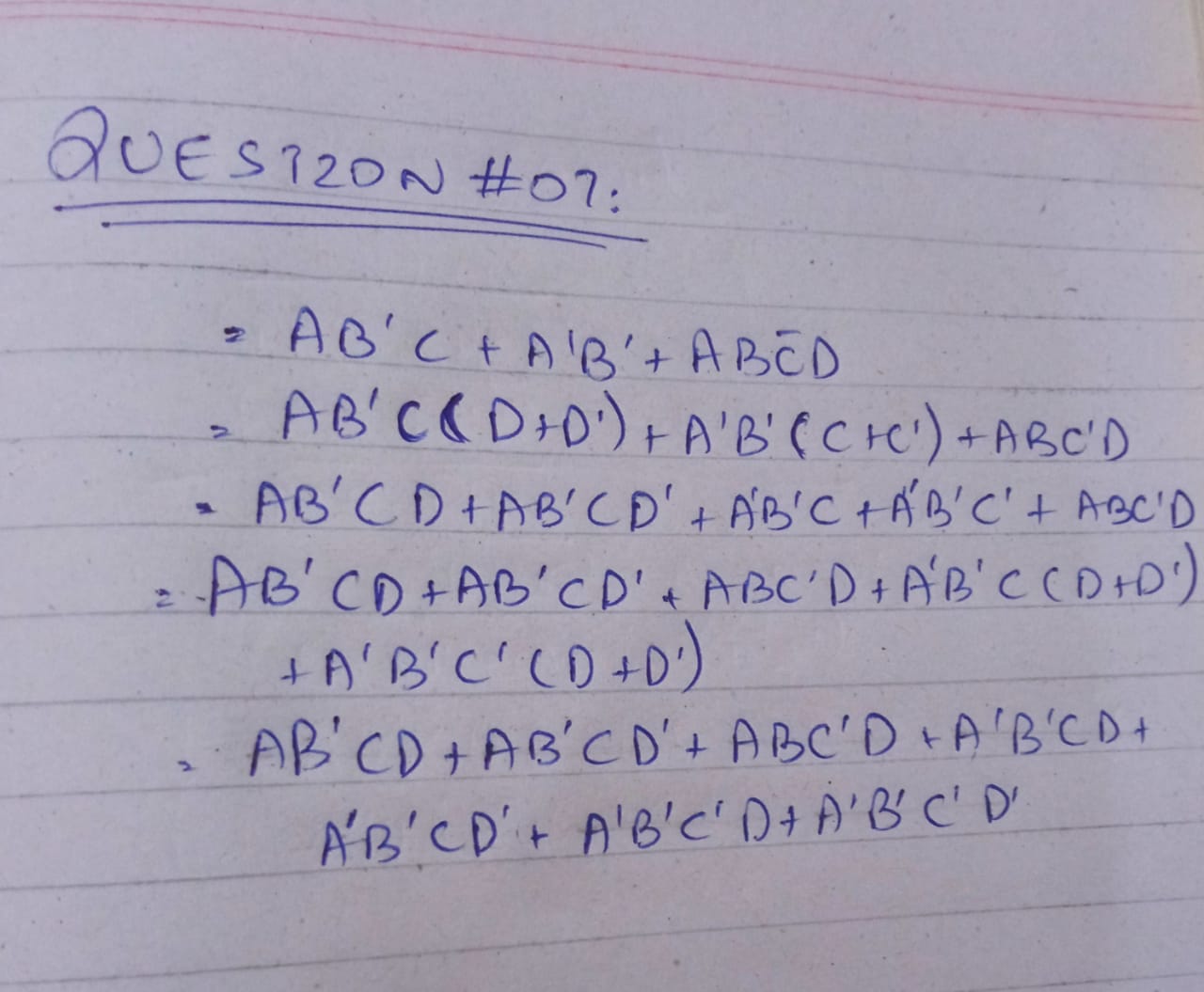


Circuit Diagram:

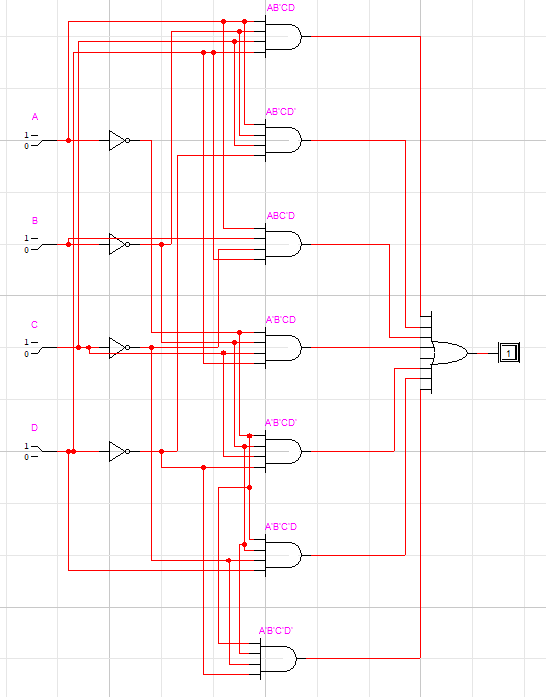


**Question 07**

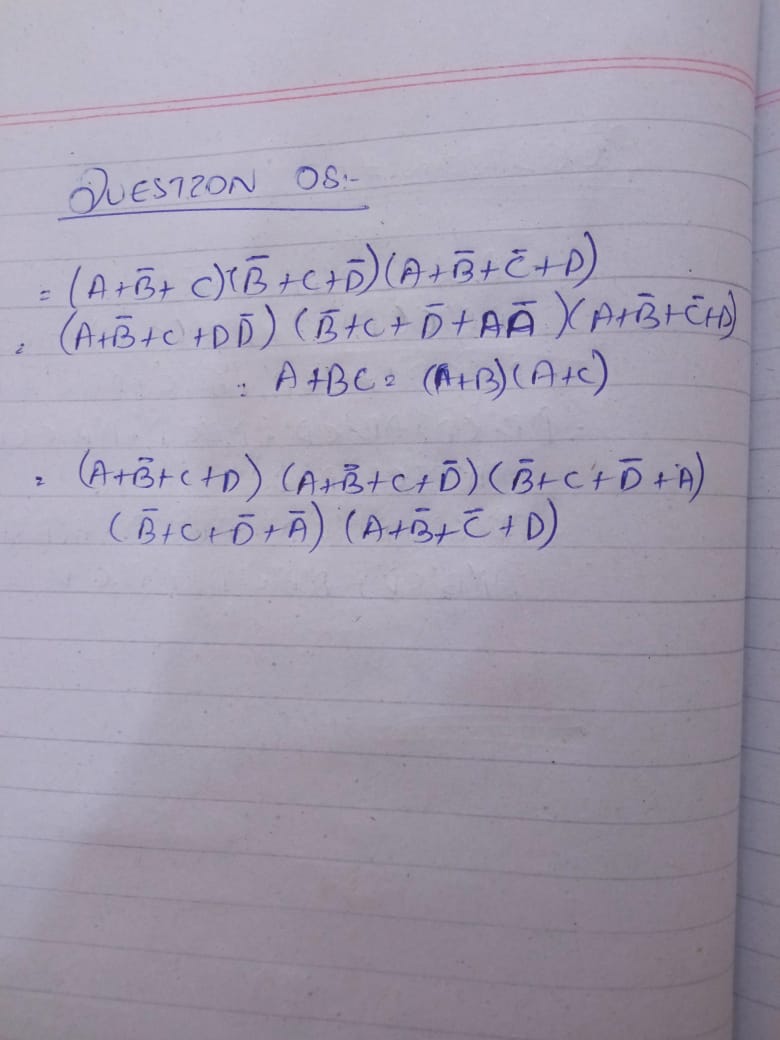
Simplification:

****

Circuit Diagram:

****

**Question 08**

Simplification: 

Circuit Design:

